

Madanapalle Institute of Technology & Science, Madanapalle-517 325
(UGC-Autonomous)
Department of Electrical and Electronics Engineering
Individual Faculty Time Table 2026-27 (ODD) I Semester (W.E.F: 15.07.2026)

Dr Kamal Basha

DAY/TIME	9.10-10.10AM	10.10-11.10AM	11.10-12.10PM	12.10 - 1.00	1.00-2.00PM	2.00-3.00PM	3.00-4.00PM	4.00-5.00PM
MON				LUNCH				
TUE			DCMT -II EEE					
WED			DCMT -II EEE					
THU			DCMT -II EEE					
FRI								
SAT								

Dr. Manavalan G

DAY/TIME	9.10-10.10AM	10.10-11.10AM	11.10-12.10PM	12.10 - 1.00	1.00-2.00PM	2.00-3.00PM	3.00-4.00PM	4.00-5.00PM
MON	IML-IV EEE	ECA - II EEE		LUNCH				
TUE		IML-IV EEE				ECA - II EEE		
WED					IML-IV EEE			
THU		ECA - II EEE						
FRI	ST for EE	ST for EE-II EEE						
SAT								

Dr. Vijaya Bhaskar Somu

DAY/TIME	9.10-10.10AM	10.10-11.10AM	11.10-12.10PM	12.10 - 1.00	1.00-2.00PM	2.00-3.00PM	3.00-4.00PM	4.00-5.00PM
MON	Analog and Digital Circuits Laboratory - III EEE			LUNCH	AE-II EEE			
TUE	Analog and Digital Circuits Laboratory - III EEE					ESD - III EEE		
WED	AE-II EEE		DC-III EEE			ESD - III EEE		
THU	AE-II EEE				ESD - III EEE			
FRI			DC-III EEE		Analog circuits lab - II EEE			
SAT	DC-III EEE							

Dr. Sarat Kumar Mishra

DAY/TIME	9.10-10.10AM	10.10-11.10AM	11.10-12.10PM	12.10 - 1.00	1.00-2.00PM	2.00-3.00PM	3.00-4.00PM	4.00-5.00PM
MON	Analog and Digital Circuits Laboratory - III EEE			LUNCH	OE-I (III)			
TUE	Analog and Digital Circuits Laboratory - III EEE				OE-I (III)			
WED		PS II - III EEE			OE-I (III)	LAT-IV EEE		
THU	PS II - III EEE				Digital Systems Lab - II EEE			
FRI		LAT-IV EEE			PS II - III EEE	LAT-IV EEE		
SAT								

Dr. Satish Kumar Ramoji

Day Session Regular Timings								
DAY/TIME	9.10-10.10AM	10.10-11.10AM	11.10-12.10PM	12.10 - 1.00	1.00-2.00PM	2.00-3.00PM	3.00-4.00PM	4.00-5.00PM
MON		PE Lab - III EEE		LUNCH	OE-I (III)			
TUE		PE Lab - III EEE			OE-I (III)			
WED	PE - III EEE	Tinkering lab III CSE I			OE-I (III)			
THU						PE - III EEE		
FRI	PE - III EEE	ST for EE-II EEE					Tinkering lab III CSE H	
SAT								

Dr. Vineet Kumar

DAY/TIME	9.10-10.10AM	10.10-11.10AM	11.10-12.10PM	12.10 - 1.00	1.00-2.00PM	2.00-3.00PM	3.00-4.00PM	4.00-5.00PM	
MON			DSD - II EEE	LUNCH	OE-I (III)	ST-III EEE			
TUE	DSD - II EEE				OE-I (III)				
WED		DSD - II EEE			OE-I (III)		ST-III EEE		
THU					Digital Systems Lab - II EEE	Tinkering Laboratory - III EEE			
FRI		Tinkering lab III CSE G			Analog circuits lab - II EEE				
SAT			ST-III EEE						

Dr. Saravanakumar R

DAY/TIME	9.10-10.10AM	10.10-11.10AM	11.10-12.10PM	12.10 - 1.00	1.00-2.00PM	2.00-3.00PM	3.00-4.00PM	4.00-5.00PM
MON		PE Lab - III EEE		LUNCH	OE-I (III)	DCMT Lab II EEE		
TUE		PE Lab - III EEE			OE-I (III)		Tinkering lab III CSE J	
WED					OE-I (III)			
THU					Tinkering lab III CSE F			
FRI								
SAT								